PRACTICAL WIRELESS

JUNE 1977

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HE General Instruments AY-3-8500 television games integrated circuit allows a complex multigame unit to be built using very few additional components. The single device provides for 4 ball games and 2 shooting games. This article will describe a unit for the 4 ball games.

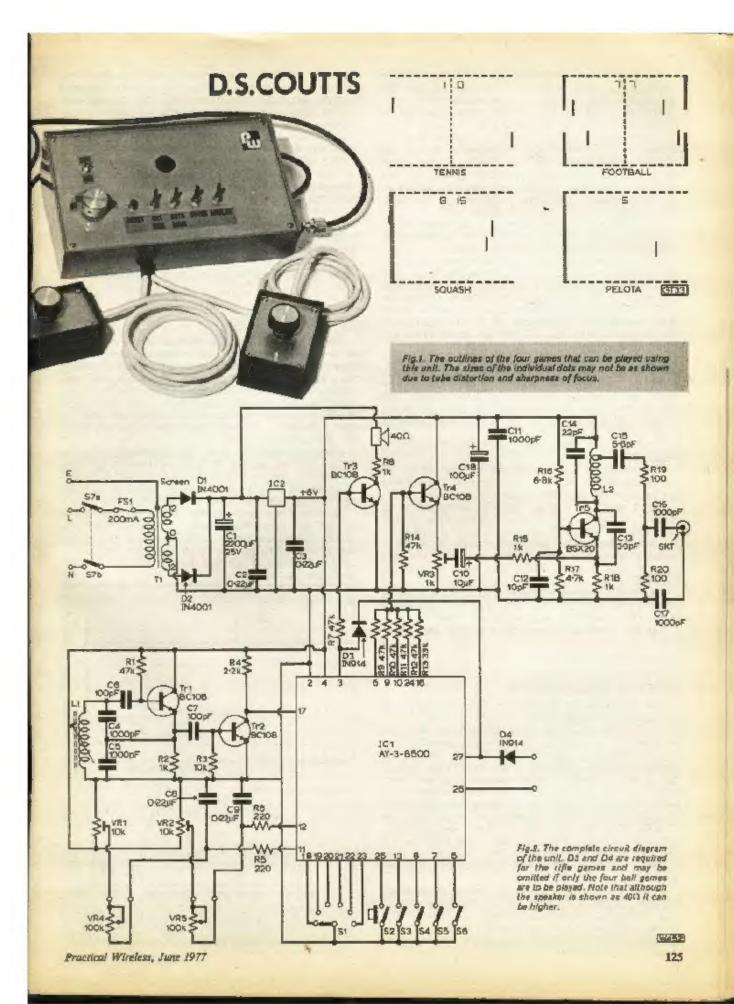
THE AY-3-8500

Before describing the construction of the unit it will be useful to give a description of the special IC. The device is in a standard 28 pin dual-in-line package with pins 1, 14, 15 and 28 not being used. Pins 2 and 4 are ground and plus 8V respectively.

* components list

Resistors		C7	100pF polystyrene
R1	47kΩ	C8	0-22µF
R2	3 k Ω	CD	0-22µF
Ra	10kΩ	C10	10µF, 10V
R4	2-2κΩ	C11	1000pF disc ceramic
R5	2200	C12	10pF plate ceramic
R6	220Ω	C13	3-3pF plate caramic
R7	47kΩ	P C14	22pF plate ceramic
R8	100	C15	5-6pF plate ceramic
Re	47\Ω	C18	1000pF disc ceramic, 250V
R10	471Ω	C17	1000pF disc ceramic, 250V
R11	47kO	C18	47µF, 10V tentalum
812	47λΩ		
R13	33kΩ	Switches	
R14	47kΩ	S!	A made A contribution
R15	1kΩ		1 pole 6 way rotary.
R16	6-8kΩ	92	Single pole, blased off, miniature push
R17	4.7kΩ	53	S.P.S.T. miniature toggle.
R18	1kQ	\$4	S.P.S.T. miniature toggle
R19	100Ω	S5	S.P.S.T. miniature toggle
R20	100Ω	\$6	S.P.S.T. miniature toggle
VR1	10kΩ standard horizontal preset	S7	D.P.S.T. miniature toggle, mains rated
VR2	10ki? standard horizontal preset		The state of the s
VR3	tkΩ standard horizontal preset	Semicondu	ctors
VR4	100kΩ linear	Tri	BC108
VR5	100kO linear	Tr2	BC108
	esistera are 1W, 5%	Tra	BC108
Part Haron C		Te4	BC108
Capacitors		Tes	BSX25
CI	2200af, 25V	D1	1N4001
CZ	0 22uF	D2	1N4001
C8	0-22µF	DS	1N914
C8 C4	1000pF polystyrene	D4	1N914
C5	1000pF polystyrene	ĪCI	AY-3-8500 G.I.
CB		IC2	MC7808 or 78L82AWC
C8	100pF polystyrene	ice	

T1, mains transformer, 2 secondaries each 0-12V at 250mA, flaing centres 53-5mm. FS1, fuse holder, chassis mounting, 20mm and 200mA fuse. PC8 from Readers PC8 Service. Speaker, miniature, 40Ω, Former 4mm with dust core. Wire 40SWG enemat covered, 1metre long. Wire 22SWG, tinned copper, 250mm long. Case sloping top, 215mm x 130mm x 47mm (front) x 79mm (back), Welford code NJSF2 Doram code 509-508. Soxes, 2 off, 85mm x 58mm x 37mm, Watford code NJHC1 Doram code 509-536. (Note, these cases provide an attractive cover but any case of similar size can be used), SKT1, coax socket. Coax cable and 2 coax plugs. 2 grommets. Mains cable. Board plus. DIL socket (see text). Heat sink for IC2



Pin 3 gives an output of 500Hs, 1kHz or 2kHz which corresponds to the ball hitting the line, the

ball hitting the bat and a scoring signal.

The ball will bounce off the correct bat at an angle determined by the condition of pin 5. When it is left open circuit the angle is constant over the whole bat except that the reflection is 'up' in the upper section of the bat and 'down' in the lower section. When pin 5 is connected to ground the bat is divided into four vertical sections. The angles in the centre two sections are as before but the outer two reflections are at steeper angles.

Pin ? controls the ball speed. If left open circuit the ball travels slowly (suitable for beginners) if

connected to ground the ball speeds up.

Automatic or manual service is selected by pin 8. If the ball leaves the court when pin 6 is open circuit it will remain off until the pin is grounded momentarily. If pin 8 is left switched to ground the ball will serve automatically, travelling in the direction that it left the ground (ie. if the right hand player scores then the ball will be served from the right band side).

Pin 11 is the right player input and an R/C network on this pin controls the vertical position of the right bats, Pin 12 provides similar control for the left hand player. The size of the bats is controlled by pin 13. When switched to ground the bat size is half that resulting

from leaving it open circuit.

Pin 17 is the clock input and a 2MHz signal is

required.

The choice of games is made by pins 18 to 23, depending which of them is grounded. The display for each of the four ball games is given in Fig. 1.

Pin 2 provides a reset facility. Switching it to ground resets the score to zero and when the ground connection is removed the ball will be served from the left. When either player scores 15 the game is finished. Both bats become transparent to the ball and the score remains static. The game is restarted by pushing and releasing the reset button.

Pins 6, 9, 10, 16 and 24 are outputs corresponding to ball, right bat, left bat, sync and field/score out-

puts. Their functions are explained later.

The two shooting games utilise pins 28 and 27.

CIRCUIT DESCRIPTION

The complete circuit diagram circuit diagram of

the unit is given as Fig. 2.

The secondary of the mains transformer is given as 12-0-12V. In fact, the transformer specified has two secondaries, each of 12V. These need to be wired in series (ie, the OV of the first to the 12V of the second) and the ground connected to the junction. D1 and B2 provide full wave rectification, smoothing is by C1 and the resultant DC is regulated by IC2. High frequency decoupling is provided by C2 and C3 on the input and output of the regulator. C11 and C18 give additional decoupling to the 8V rail.

Trl is a 2MHz oscillator, its output being buffered

by Tr2 before being fed to ICl.

VRI, VR4, R5 and C8 are the timing components for the right hand player. VR4 is the hand control and changes the charging current of C8 thus moving the vertical position of the bat. VRI sets the voltage towards which C8 charges and alters the sweep of

the bat. R5 limits the current during the discharge period at frame flyback. The corresponding controls for the left hand player are VR2, VR5, R6 and C9.

for the left hand player are VR2, VR5, R6 and C9.

Tr3 drives the speaker, with R8 limiting the volume. If greater volume is required the value of R8 can be reduced but, on the prototypes, it was sufficient for most domestic uses.

The video and sync outputs are summed by R9 to R13 and drive Tr4, an emitter follower. VR3 in the emitter allows the modulation level of Tr5 to be

adjusted

Tr5 is VHF oscillator operating at about 170MHz with harmonics extending into the UHF band. The output is divided by R19 and R20 and the unit is DC isolated from the display monitor by C16 and C17.

Switches 31 to S6 control the games and functions already described, (see Fig. 7 for details also).

CONSTRUCTION

Before commencing construction of the electronics it is necessary to drill the mounting holes in the case. The board can be used as a template to ensure they are in the right position. The board will ultimately sit with the modulator section at the front right and with its right hand edge about 10mm from the right side of the case. This allows the leads of C16 and C17 to be kept short. The mounting holes for the hoard are 6 BA.

Having drilled the base, the remaining holes can be marked out as Fig. 3. These holes will be sized to suit the grommets and the coax socket. It is also advisable to drill a series of 5mm holes in the base

and in the back to assist ventilation.

Mark the top panel as Fig. 4 and drill the holes to suit the components used. A suitable hole size for the speaker is 20mm, Label the panel and mount the switches. The speaker is glued to the panel using a rapid-cure epoxy but first glue a piece of speaker fret over the hole.

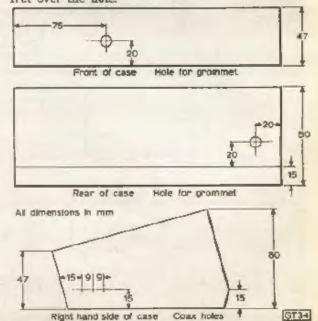


Fig.3. Suitable drilling hele locations and sizes are shown for the box specified. The single hole in the front of the case is for both the bat control leads. It is easier to identify the bats if two holes are made, one at each and of the front.

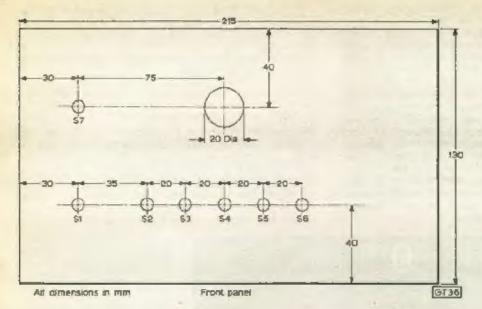


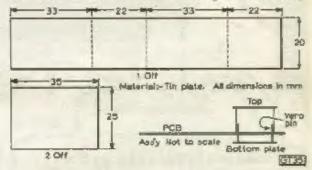
Fig.4. The drilling details for the lid of the case specified. If a different case is used, ensure that there is sufficient depth beneath the panel for the switches.

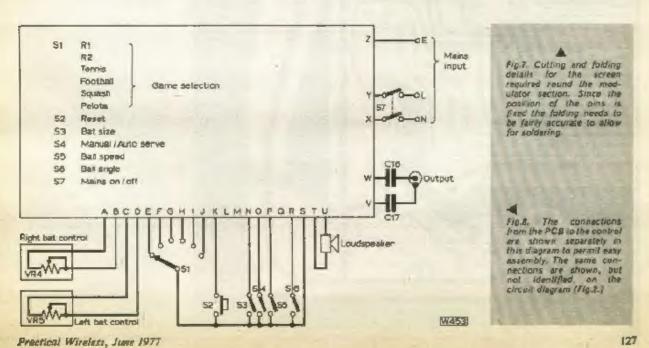
Most of the components are mounted on a single sided PCB. Fig. 5. shows the wiring pattern and Fig. 6 the component locations. The former for L1 is stuck in position, again using rapid-cure epoxy, and when the adhesive has set, 50 turns of 40 SWG are wound onto the former. After soldering the ends as shown in Fig. 6, the screening can is fitted. It is better to wind the coil after the former is fixed to the board because although it is difficult to wind it is even more difficult to hold the winding in place whilst the epoxy sets.

The remaining components, except for the coll L2 and IC1 are mounted (not forgetting the wire link). L2 is 3 complete turns of 22 SWG tinned copper wire wound on a former so that the diameter of the coll is 6mm (14 inch) after the former is removed. A short length of wire is soldered to the coll 34 of a turn from one end. The coll is mounted in position and the screen is soldered to the four pins at the

corners of the oscillator section. Leave the top of the screen off. It may be necessary to adjust the frequency. Details of the screen are given as Fig. 7.

It is strongly recommended that a socket or Soldercon pins are used for ICl. This device is of MOS construction and is liable to damage if incorrectly





handled. If for any reason it is necessary to solder the IC directly into the board the soldering iron must have an earthed tip and a heat shunt should be used on the pin being soldered. The socket need only be a 24 lead type since the end pins on each side are unused. If a socket is used do not insert ICl yet.

Recheck the board for solder bridges and for good soldered joints and then mount it in the box. The dimensions given for the box specified allow for form (4 inch) spacers beneath the board.

Connect the off-board components to the pins as shown in Fig. 8. The leads for the bat controls should be about 1 metre long and be twin cable. The variable resistors are mounted in small boxes.

Some plas on the board are not used in this 4 game unit although S1 is shown wired for all 8 games. Pins E and F can be left unwired.

A suitable length of coaxial cable needs to be terminated at both ends and a mains cable, preferably terminated with a 13A ring main plug, completes the wiring.

Now remove the IC from its protective foam and insert it into the holder.

SETTING UP

Set the core of L1 flush with the top of the former and space L2 to approximately 7-5mm long. Set VRI, VR2 and VR5 to mid position, select football on the games unit and select auto serve. Switch the unit on, push and release the reset button S2. Tones should be heard at regular intervals from the unit as the ball hits the game boundaries etc.



Fig.5. The wiring pattern of the printed clicual board shown full size. For those intending to make their own boards we would stress the need for great care in laying out the IC pada and the modulator section. The earth section could be increased to conserve the election.

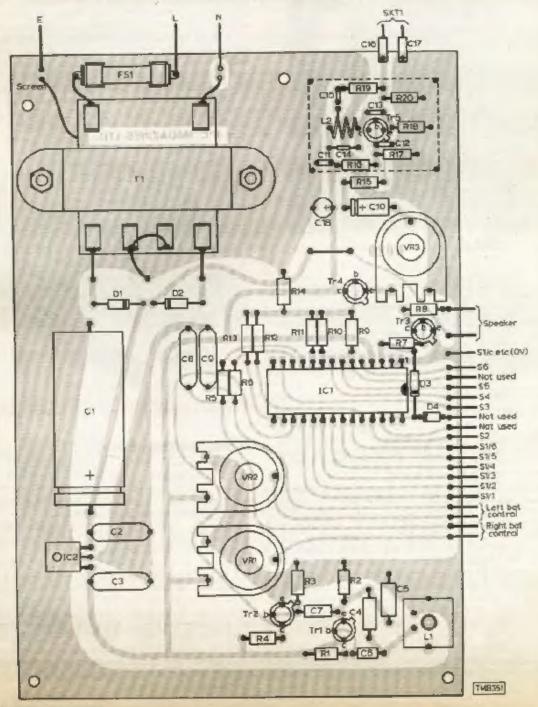
Fig.8. The component layout and orientation for the PCB. Although the outputs are not identified they conform to those shown in Fig.8. Note the light layout of the modulator section and hence the need for careful culting and folding of the screen. If all is well, switch the monitor on, allow it to warm up, select a spare U.H.F. channel and plug the games unit into the aerial socket. Carefully tune the viewer until the signal from the games unit appears on the screen, it may only consist of white streaks covering the screen. Slowly screw the dust core into L1 until the streaks on the screen resolve themselves into the outline of the football pitch as shown in Fig. 1. It may be necessary to re-adjust the viewer, tuning again for a good signal. Several signals will be picked up throughout the U.H.F. band, choose the best one.

When the reset button is pushed and released the football field should appear locked solid on the

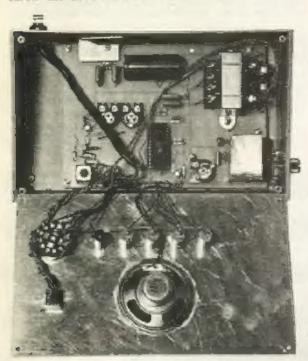
screen, if it does not adjust L1 slightly one way or the other until it locks into sync each time it is reset. When the field is stable on the screen, VR3 may be adjusted anticlockwise to increase the contrast and the brightness control on the viewer may be turned down slightly to reduce the background. (If VR3 is turned too far anticlockwise sync will be lost.)

If the signal from the unit is close to a TV station L2 may be altered in length to move the games frequency up or down the band a little, then the screening cover can be fitted.

Check that the bats sweep across the screen. VR1 and VR2 may be turned anticlockwise to reduce the



sweep of the bats but if they are turned too far the bats will disappear since the ramp will not reach the I.C. trigger level. Check the other games and the various switch functions and if it all checks out, screw the lid on the case.



A general photograph of the unit with the lid removed. The returner for the mains cable has been removed to allow the lid to be inverted. It should be positioned to arold too much cable inside the box when re-assembled. The short lends of the capacitors caupling the boxed to the output socket can be identified. These two capacitors prevent any possibility of mains being fed to the unit from the television chassis if the set has its mains reversed.

FAULT FINDING

If the unit does not work, check all wiring very carefully. Check the +8 Volts supply to the LC, modulator and the clock generator. If an oscilloscope is available, check that the oscillator is working and producing sufficient drive for the buffer, Tr2, and is approximately 2MHz.

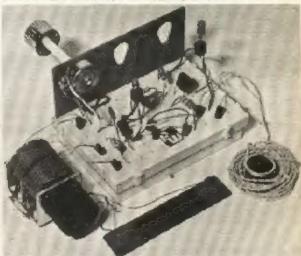
If the L.C. has +8 Volts and a good clock signal, check the sync output. This should consist of negative going pulses 4µS wide, at 64 µS intervals with an approx 300 µS frame pulse every 20 mS. If this is present, check the field and score output on pin 24, this should be a positive-going pattern repeating every 20 mS. If this is present, check that both these signals appear at the wiper of VR3. If all is well up to this point and the unit is still not working, check the modulator construction very carefully. As it is difficult to check if it is working it is possibly easiest to substitute a new transistor for Tr5.

Watford Electronics have affered to keep their special reduced price for the AY 3-8500 open to readers of Practical Wireless until the end of June 1977.

S-DeChology-continued from page 122.

of the wire, especially the tapping loop and tin these connections before soldering on some wired S-DeC plugs. If desired, the wire from the coil itself may be plugged into the S-DeC but you will still need to use some form of single core wire from the tapping point to the S-DeC because the twisted wire, being double thickness, will not easily plug into the S-DeC and may even damage it if forced. A second ferrite aerial/coil was wound on a 125mm, length of ferrite rod, 9mm in diameter and also gave good results.

The tuning capacitor used in the prototype was a twin gang 500pF + 500pF. There is no reason why a single capacitor of either 500pF or 350pF should not be used, but this will make some difference to the actual tuning scale covered by the receiver. The twin gang does give a useful opportunity for experiment. For example, with both sections connected in parallel—as shown in the circuit diagram of Fig. 1, one has a 1,000pF (or 1nF) variable tuning capacitor. By "unlinking" the two sections of VCI/VC2 to leave only one of them in circuit, one has a 500pF tuning capacitor. Again, connecting the two sections in series gives a 250pF tuning capacitor.



Rear view pholograph showing the actual receiver, comprising S-DeC front mounted potentiameter, and external components.

Modification Ideas

Experimentally minded constructors may care to try altering the coil. For example, using only 24 turns tapped at 2 turns gave all sorts of 'funny' foreign stations. It may even be possible to obtain good results high up in the short wave bands.

This little receiver runs from just SV. This is the optimum voltage. If you use a higher voltage the performance will be degraded. The current drawn by the prototype (using SV) was 1-5mA which suggests that batteries should last for a very long time indeed. It may also be possible to run the receiver from two, small rechargeable batteries which could be kept 'topped up' from solar cells using sunlight. Perhaps the set may be happy to work directly from solar cells?

Various component changes and modifications were tried but it's left to the constructor to experiment for himself, with different component values, to obtain the best possible results.

WATFORD ELECTRONICS

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9A78 17	0 4A100V 710	
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